Please charge our credit card for the calculated fee of $84.00. PTO Form 2038 is attached.

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge any underpayment or credit any overpayment to Deposit Account No. 50-2215.

CONTINGENT EXTENSION REQUEST

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 CFR 1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 CFR 1.135. The fee under 37 CFR 1.17 should be charged to our Deposit Account No. 50-2215.

SUMMARY OF AMENDMENTS

In the Specification:

There are no amendments to the specification.

In the Claims:

A complete listing of all claims ever present in this case in ascending order with status identifier is presented in a separate section.

COMPLETE LISTING OF CLAIMS
IN ASCENDING ORDER WITH STATUS INDICATOR

Claim 1. (Currently Amended) A frame-relay frame processing device for reassembling a frame-relay frame into an Asynchronous Transfer Mode (ATM) cell, wherein said device receives a frame-relay frame and writes said frame to a memory location shifted from ~~an end~~ a top ~~of the next available memory location in the~~ a frame buffer.

Claim 2. (Currently Amended) A frame-relay frame processing device according to claim 1, wherein a size of the shift from ~~an end~~ the top the frame buffer ~~of the next available memory location~~ is determined for each connection.

Claim 3. (Previously Amended) A frame relay circuit for reassembling a frame-relay frame into an Asynchronous Transfer Mode (ATM) cell comprising:

a processor for determining for each connection a size of a shift by which said frame is to be shifted from ~~an end~~ a top of ~~the next available memory location in~~ a frame buffer;

a frame receiver for receiving said frame through said connection;

a memory for storing said received frame at a location shifted from ~~an end~~ the top of ~~the next available memory location in~~ the frame buffer by said shift size; and

a segmentation and reassembling device for reassembling said frame into said ATM cell.

Claim 4. (Previously Amended) A frame relay circuit according to claim 3, wherein, for each connection, said processor writes a data link connection identifier (DLCI) and said shift size into a connection table, and retrieves said shift size from said connection table using said DLCI as a key.

Claim 5. (Previously Amended) A frame relay circuit according to claim 3, wherein said frame received by said frame receiver is transmitted to said memory through direct memory access.

Claim 6. (Currently Amended) A method for reassembling a frame-relay frame into an Asynchronous Transfer Mode (ATM) cell comprising the steps of:

determining a shift size for each connection by which said frame is to be shifted from ~~an end~~ a top of ~~the next available memory location in~~ a frame buffer;

receiving said frame and writing said frame starting from an address shifted by said shift size; and

reassembling said frame into an ATM cell.

Claim 7. (Previously Amended) A method according to claim 6, further comprising the steps of:

writing a data link connection identifier (DLCI) and said shift size into a connection table for each connection; and

retrieving said shift size from said connection table using said DLCI as a key.

Claim 8. (Original) A method according to claim 6, wherein said received frame is transmitted to said memory through direct memory access.

Claim 9. (Currently Amended) A computer readable medium containing program instructions for reassembling a frame-relay frame into an Asynchronous Transfer Mode (ATM) cell, the program instructions including instructions for performing the steps comprising:

determining a shift size for each connection by which said frame is to be shifted from ~~an end~~ a top address of ~~the next available memory location in~~ a frame buffer;

receiving said frame and writing said frame starting from an address shifted by said shift size; and

reassembling said frame into an ATM cell.

Claim 10. (Previously Amended) A computer readable medium according to claim 9, wherein said program instructions include instructions for:

writing a set of a data link connection identifier (DLCI) and said shift size into a connection table for each connection; and

retrieving said shift size in said connection table using said DLCI as a key.

Claim 11.   (Original)   A computer readable medium according to claim 9, wherein said received frame is transmitted to said memory through direct memory access.